

Notice of References Cited	Application/Control No. 10/760,962	Applicant(s)/Patent Under Reexamination WILCOX ET AL.	
	Examiner Leigh Marie Garbowski	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,434,704	08-2002	Dean et al.	713/320
*	B	US-6,643,829	11-2003	Borkovic et al.	716/3
*	C	US-6,536,024	03-2003	Hathaway, David J.	716/6
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	T. LANG et al., "Individual Flip-Flops with Gated Clocks for Low Power Datapaths," IEEE Trans. on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 44, No. 6, June 1997, pages 507-516.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.